

The Verilog Hardware Description Language: 5th (Fifth) Edition By Philip R. Moorby Donald E. Thomas

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Verilog

Verilog: Verilog Analog Mixed-Signal (verilog-ams) Working Group; Verilog P1364 Standard for Verilog Hardware Description Language (IEEEVerilog)

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Verilog, standardized as IEEE 1364, is a hardware description language (HDL) used to model electronic systems. It is most commonly used in the design and verification

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